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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/717,934	11/21/2003	Makoto Terui	OKI.597	7542
20987	7590	03/07/2005		EXAMINER
				BREWSTER, WILLIAM M
			ART UNIT	PAPER NUMBER
				2823

DATE MAILED: 03/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/717,934	TERUI ET AL. <i>RM</i>
	Examiner	Art Unit
	William M. Brewster	2823

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 19 January 2005.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-18 is/are pending in the application.
4a) Of the above claim(s) 4,5 and 7-18 is/are withdrawn from consideration.
5) Claim(s) _____ is/are allowed.
6) Claim(s) 1-3 and 6 is/are rejected.
7) Claim(s) _____ is/are objected to.
8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a))

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 112103: 062504

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ .

5) Notice of Informal Patent Application (PTO-152)

6) Other: _____

DETAILED ACTION

Election/Restrictions

Applicant's election without traverse of Species I, claims 1-3, 6 in the reply filed on 19 January 2005 is acknowledged.

Claims 4, 5, 7-18 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected species. Election was made **without** traverse in the reply filed on 19 January 2005.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-3, 6 are rejected under 35 U.S.C. 102(e) as being anticipated by Farnworth et al., US Publication No. 2004/0113283 A1, a divisional of US Application No. 10/094,161 filed on March 6, 2002.

Farnworth anticipates, a method of fabricating a semiconductor device comprising: in p. 14, ¶ 220-29,

in fig. 13A, providing a semiconductor wafer 10E having a first surface 20E and a second surface 22E

opposite of the first surface, the first surface having a plurality of circuit elements (not shown, but with connecting terminals at 18E, each of which is defined by scribe lines formed in the semiconductor wafer (see below), p. 14, ¶ 220;

in fig. 13C, forming a sealing resin 36E, on the first surface of the semiconductor wafer; forming a plurality of external terminals on the first surface of the semiconductor wafer, p. 14, ¶ 223, in fig. 13D, wherein the external terminals 24EP, and fig. 13G, 42E, respectively electrically connect to the circuit elements and project from the sealing resin;

in fig. 13B, selectively forming a heat spreading material, exposed half of 22E, on the second surface of the semiconductor wafer, wherein the scribe lines are exposed, through 84E, from the heat spreading material, wherein the forming of spaces 86E forms scribe lines in the wafer defining the circuits;

and, in fig. 13F, separating the semiconductor wafer at the scribe line after the heat spreading material is formed on the second surface of the semiconductor wafer, p. 14, ¶ 229;

limitations from claim 2, the method according to the claim 1, further comprising polishing the second surface of the semiconductor wafer before said forming the heat spreading material, p. 7, ¶ 14;

limitations from claim 3, the method according to the claim 1, in fig. 13B, further comprising forming a heat conductor, surface portion of 84E, on the second

surface of the semiconductor wafer, before said forming the heat spreading material, exposed portion of 84E.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to William M. Brewster whose telephone number is 571-272-1854. The examiner can normally be reached on Full Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

William M. Brewster

1 March 2005
WB